1/6

FIG. 1a

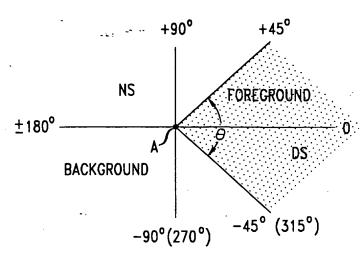
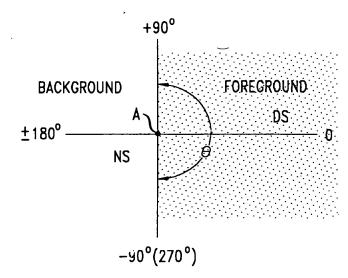
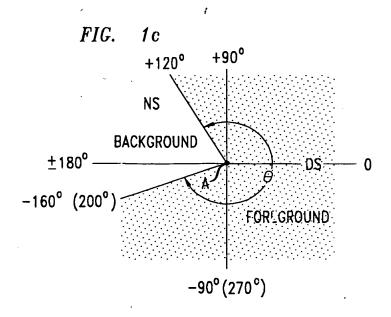


FIG. 1b





2/6

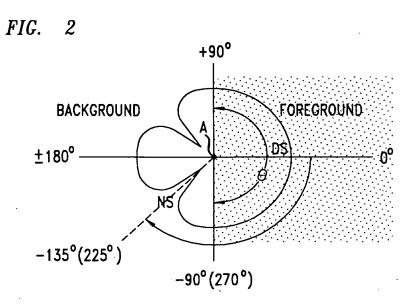
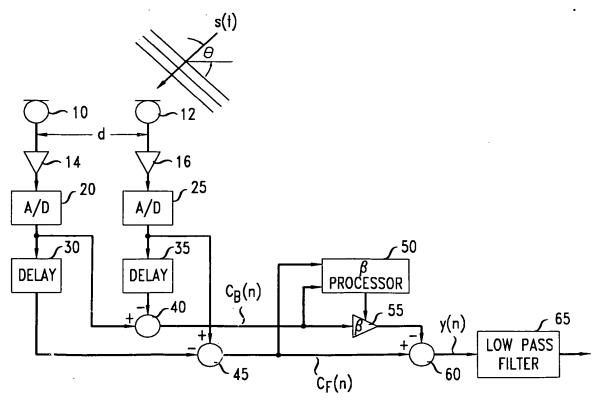
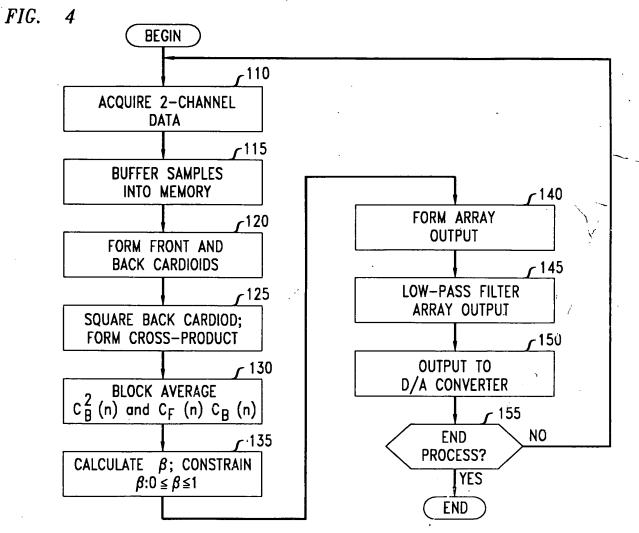
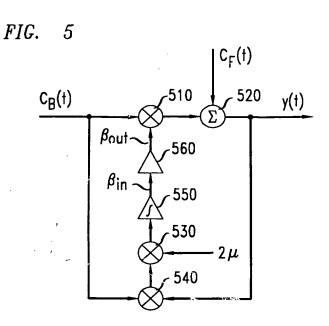


FIG. 3







4/6

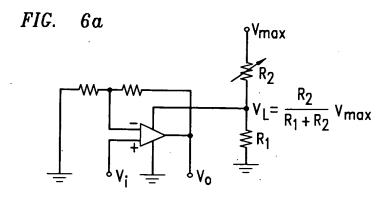


FIG. 6b

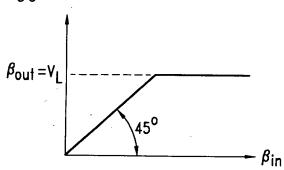


FIG. 7 -220 $\beta_{\text{M/2}}$ β PROCESSOR $-\beta_0$ 212 -215 -226 FB C_B(n) ∠210 235 DELAY y(n)

FIG. 8

